

PTO/SB/08A (08-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet 1 of 1

Complete if Known

Application Number	10/776,438
Filing Date	2/10/2004
First Named Inventor	Gunn et al.
Art Unit	2874
Examiner Name	(not yet assigned)
Attorney Docket Number	LUX-P021

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
JMB	1	US- 6,285,813	09-04-2001	Schultz et al.	
JMB	2	US- 5,101,459	03-31-1992	Sunagawa	
JMB	3	US- 5,033,812	07-23-1991	Yoshida et al.	
JMB	4	US- 5,436,991	07-25-1995	Sunagawa et al.	
JMB	5	US- 5,200,939	04-06-1993	Nishiwaki et al.	
JMB	6	US- 5,132,843	07-21-1992	Aoyama et al.	
JMB	7	US- 5,742,433	04-21-1998	Shiono et al.	
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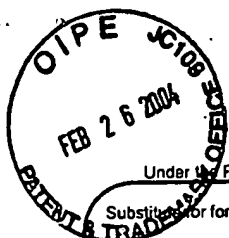
FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ *Number ⁴ *Kind Code ⁵ (if known)				

Examiner Signature	<i>J. Martin B...</i>	Date Considered	01/23/06
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Examiner Name	"
Attorney Docket Number	LUX - P021

Sheet	1	of	1
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
JMB	1	"LOW LOSS NANOPHOTONIC WAVEGUIDES AND RING RESONATORS IN SILICON-ON-INSULATOR"; BEATS, ET AL	
JMB	2	"LARGE-SCALE PRODUCTION TECHNIQUES FOR PHOTONIC NANOSTRUCTURES"; BOGAERTS, ET AL	
JMB	3	"CMOS-COMPATIBLE PLANAR SILICON WAVEGUIDE-GRATING-COUPLER PHOTODETECTORS FABRICATED ON SILICON-ON-INSULATOR (SOI) SUBSTRATES"; S.M. CSUTAK, S. DAKSHINA-MURTHY &	
JMB		JOE C. CAMPBELL; IEEE JOURNAL OF QUANTUM ELECTRONICS, VOL.38 NO. 5 MAY 2002	
JMB	4.	"BURIED-OXIDE SILICON-ON-INSULATOR STRUCTURES II: WAVEGUIDE GRATING COUPLERS"; EMMONS, ET AL; IEEE JOURNAL OF QUANTUM ELECTRONICS, VOL.28 NO. 1 JANUARY 1992.	
JMB	5	"INTEGRATED OPTICS FOR OPTICAL HEAD APPLICATIONS"; OCHIAI ET AL; ELECTRONICS LETTERS 18TH AUGUST 1994 VOL. 30 NO. 17, PAGE 1403-1404	
JMB	6	"EFFECTS OF GRATING HEIGHTS ON HIGHLY EFFICIENT UNIBOND SOI WAVEGUIDE GRATING COUPLERS"; ANG, ET AL; IEEE PHOTONICS TECHNOLOGY LETTERS VOL.12 NO. 1 JAN 2000	
JMB	7	"MODE MATCHING TECHNIQUE FOR HIGHLY EFFICIENT COUPLING BETWEEN DIELECTRIC WAVEGUIDES AND PLANAR PHOTONIC CRYSTAL CIRCUITS"; SANCHIS, ET AL	
JMB	8	"BURIED-OXIDE SILICON-ON-INSULATOR STRUCTURES II: WAVEGUIDE GRATING COUPLERS"; EMMONS, ET AL; IEEE JOURNAL OF QUANTUM ELECTRONICS, VOL 28 NO.1 JANUARY 1992	
JMB	9	"NON UNIFORM GRATING COUPLERS FOR COUPLING OF GAUSSIAN BEAMS TO COMPACT WAVEGUIDES"; WEST, ET AL; PUBLISHED IN THE INTEGRATED PHOTONICS RESEARCH TECHNICAL DIGEST 1994	

Examiner Signature	<i>J Martin Be</i>	Date Considered	01/23/06
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